



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit: 2661
Serial No.: 10/684,078 Examiner: Unassigned
Filed : October 10, 2003 Assignee: Intel Corporation
Title : METHOD AND APPARATUS FOR GIGABIT PACKET ASSIGNMENT
FOR MULTITHREADED PACKET PROCESSING

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first Office action on the merits. No fee is required.

The documents cited on the Forms PTO-1449 are in the English language, with the exception of item noted below. Item BX (EP 0959602) is in the French language and is provided with an English language equivalent (item BM). Hence, in accordance with the requirements of 37 C.F.R. §1.98, as amended effective

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

March 6, 2006

Date of Deposit

Signature

Kelly M. Smith

Kelly M. Smith

Typed or Printed Name of Person Signing
Certificate

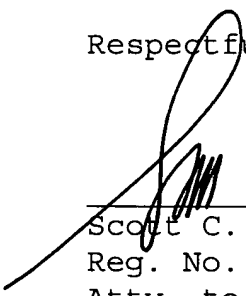
March 16, 1992, no further explanation of the listed items are necessary.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: March 6, 2006



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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-132002	Application No. 10/684,078
		Applicant Gilbert Wolrich et al.	
		Filing Date October 10, 2003	Group Art Unit 2661

**Information Disclosure Statement
by Applicant**
(Use several sheets if necessary)

(37 CFR §1.98(b))

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	3,736,566	5/29/1973	Anderson et al.			
	AB	4,709,347	11/24/1987	Kirk, David L.			
	AC	4,890,218	12/26/1989	Bram			
	AD	4,890,222	12/26/1989	Kirk, David L.			
	AE	5,251,205	10/5/1993	Callon			
	AF	5,404,464	4/4/1995	Bennett			
	AG	5,404,469	4/4/1995	Chung			
	AH	5,459,843	10/17/1995	Davis			
	AI	5,485,455	1/16/1996	Dobbins et al.			
	AJ	5,515,296	5/7/1996	Agarwal, Rohit			
	AK	5,581,729	12/3/1996	Nistala et al.			
	AL	5,630,130	5/13/1997	Perotto et al.			
	AM	5,651,002	7/22/1997	Van Seters et al.			
	AN	5,754,764	5/19/1998	Davis, et al.			
	AO	5,768,528	6/16/1998	Stumm			
	AP	5,781,551	7/14/1998	Born			
	AQ	5,809,237	9/15/1998	Watts et al.			
	AR	5,898,701	4/27/1999	Johnson, David L.			
	AS	5,933,627	8/3/1999	Parady et al.			
	AT	5,938,736	8/17/1999	Muller et al.			
	AU	6,032,190	2/29/2000	Bremer et al.			
	AV	6,067,300	5/23/2000	Baumert et al.			
	AW	6,141,677	10/31/2000	Hanif et al.			
	AX	6,272,520	8/7/2001	Sharangpani et al.			
	AY	6,282,169	8/28/2001	Kiremidjian			
	AZ	6,295,600	9/25/2001	Parady et al.			
	BA	6,311,261	10/30/2001	Chamdani et al.			

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-132002	Application No. 10/684,078
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U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	BB	6,393,026	5/21/2002	Irwin			
	BC	6,424,659	7/23/2002	Viswanadham et al.			
	BD	6,477,562	11/5/2002	Nemirovsky et al.			
	BE	6,484,224	11/19/2002	Robins et al.			
	BF	6,526,451	2/25/2003	Kasper et al.			
	BG	6,553,406	4/22/2003	Berger et al.			
	BH	6,570,850	5/27/2003	Gutierrez et al.			
	BI	6,629,236	9/30/2003	Aipperspach et al.			
	BJ	6,631,422	10/7/2003	Althaus et al.			
	BK	6,675,190	1/6/2004	Schabernack et al.			
	BL	6,678,746	1/13/2004	Russell et al.			
	BM	6,697,379	2/24/2004	Jacquet et al.			
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	BO	6,925,637	8/2/2005	Thomas et al.			
	BP	6,952,824	10/4/2005	Hooper et al.			
	BQ	2004/0148382	7/29/2004	Narad et al.			
	BR	2004/0039895	2/26/2004	Wolrich et al.			
	BS	2004/0054880	3/18/2004	Bernstein et al.			
	BT	2004/0073728	4/15/2004	Wolrich et al.			
	BU	2004/0073778	4/15/2004	Adiletta et al.			
	BV	2004/0098496	5/20/2004	Wolrich et al.			
	BW	2004/0109369	6/10/2004	Wolrich et al.			

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
	BX	EP 0 959 602	11/24/1999	EP			Yes	No
	BY	WO 01/16718	3/8/2001	PCT				

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Foreign Patent Documents or Published Foreign Patent Applications								
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							Yes	No
	BZ	WO 01/17179	3/8/2001	PCT				
	CA	WO 03/030461	4/10/2003	PCT				
	CB	WO 98/20647	5/14/1998	PCT				

X+ = An English language equivalent is provided.

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	CC	Chandranmenon, G.P. et al., "Trading Packet Headers for Packet Processing," IEEE/ACM Transactions on Networking 4(2): 141-152 (April 1996)
	CD	Dictionary of Computer Words: An A to Z Guide to Today's Computers, Revised Edition, Houghton Mifflin Company: Boston, Massachusetts, pp. 220, (1995)
	CE	Digital Semiconductor 21140A PCI Fast Ethernet LAN Controller, Hardware Reference Manual, Digital Equipment Corporation, pages i-x, 1-1 through 1-5, 2-1 through 2-12, 3-1 through 3-38, 4-31 through 5-2, 6-1 through 6-24, (March 1998)
	CF	News.Com, August 26, 1999. "Enterprise Hardware, Intel Expected to Unveil New Networking Chip," http://new.com.com/Intel+expected+to+unveil+new+networking+chip/2100-1001_3-230315.html (accessed on 08/23/05), pages 1-5

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